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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/763,204	02/20/2001	Fabien Clermidy	034299-311	3428

7590 04/08/2004
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EXAMINER

WILSON, YOLANDA L

ART UNIT	PAPER NUMBER
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2113

DATE MAILED: 04/08/2004

11

Please find below and/or attached an Office communication concerning this application or proceeding.

87

Office Action Summary

Application No.

09/763,204

Applicant(s)

CLERMIDY ET AL.

Examiner

Yolanda Wilson

Art Unit

2113

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 15 March 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-6 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-6 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

SECOND DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-6 are rejected under 35 U.S.C. 102(b) as being anticipated by Clermidy et al. 'A New Placement Algorithm Dedicated to Parallel Computers: Bases and Application.' As appears in claim 1, Clermidy et al. discloses a step of positioning the functional elements of the logic network on pages 244-245, sections 2.3-2.6.

Clermidy et al. discloses a routing step of programming interconnecting elements on the physical network by choosing a maximum number of interconnecting elements which can be passed between two neighboring processors using a shortest track search algorithm on page 246, section 3.3.

3. As per claim 2, Clermidy et al. discloses a network functional element positioning sequence which is composed of a starting functional element and a sequence of functional elements including all functional elements is determined; for each of the functional elements it is positioned tentatively starting with its logical position then if required in case of failure in each of the positions located at a distance 1, distance 2... from the logical position of this functional element a restriction being that one and only one spare position is to be used with respect to the possible positions of previously positioned functional elements stopping when S+1 positions have been tested S being

the number of spare functional elements; if S+1 positions have been tested without success returning to the previous functional element in the positioning sequence and proceeding with the next position for this functional element on pages 244-245, sections 2.3-2.6.

4. As per claim 3, Clermidy et al. discloses when all functional elements have been positioned it is checked for each network dimension that the logical sequence is followed for each functional element pair if not the position of these functional elements are inverted on page 244, section 2.5.

5. As per claim 4, Clermidy et al. discloses the starting with the top left functional element continuing with the following functional elements to the right and below the previous functional element following a diagonal path on pages 245-246, section 3.2 and figure 5.


6. As per claim 5, Clermidy et al. discloses the network is divided into functional element blocks and starting with a starting block and going through all the blocks from one neighboring block to the next the possible positions for the functional elements of one block not including any logical position of the functional elements of previously positioned blocks on pages 244-245, sections 2.3-2.6.

7. As per claim 6, Clermidy et al. discloses the functional elements are processors on page 244, section 2.3.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yolanda Wilson whose telephone number is (703) 305-3298. The examiner can normally be reached on M-F (7:30-4:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Beausoliel can be reached on (703) 305-9713. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


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